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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/707,386

11/07/2000

Jack D. Pippin

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06/17/2004

OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.  
1940 DUKE STREET  
ALEXANDRIA, VA 22314

EXAMINER

LUU, CHUONG A

ART UNIT

PAPER NUMBER

2825

DATE MAILED: 06/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Applicati n No.

09/707,386

Applicant(s)

PIPPIN, JACK D.

Examiner

Chuong A Luu

Art Unit

2825

-- The MAILING DATE of this c mmunication appears on the c ver sheet with the corresp ndence address --

**Peri d for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-19 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 2/5/01;3/26/01.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

**DETAILED ACTION**  
**PRIOR ART REJECTIONS**

**Statutory Basis**

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

**The Rejections**

Claims 1-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Cacciatore (U.S. 4,799,176).

Cacciatore discloses a programmable electronic digital thermostat with

Respect to claims:

**(1); (12)** a fail safe sensor; halt logic to halt operation of the integrated circuit in response to the fail safe sensor indicating that a threshold temperature has been exceeded (see column 10, lines 2-66; column 5, lines 1-22);

**(2); (13)** wherein the threshold temperature is a predetermined fixed critical temperature (see column 1, lines 35-54);

**(3); (14)** wherein the halt logic is to inhibit operation of the integrated circuit by stopping a clock for the integrated circuit (see column 4, lines 1-27);

(4); (15) wherein the halt logic protects the integrated circuit without software control (see column 6, lines 48-54);

(5) a plurality of thermal sensors placed across the integrated circuit; an averaging mechanism in communication with the fail-safe sensor to calculate an average temperature from the plurality of thermal sensors (see column 3, lines 49-68; column 4, lines 1-55);

(6) further comprising clock adjustment logic in communication with the fail-safe sensor to control temperature of the integrated circuit by increasing and decreasing a clock frequency of the integrated circuit (see column 3, lines 49-68; column 4, lines 1-55);

(7) further comprising clock adjustment logic in communication with the fail-safe sensor to execute instructions to provide closed loop control of the integrated circuit clock frequency, thereby automatically reducing the temperature when overheating occurs (see column 3, lines 49-68; column 4, lines 1-55; column 5, lines 1-22);

(8) further comprising clock adjustment logic in communication with the fail-safe sensor to decrease a clock frequency of the integrated circuit in response to the fail-safe sensor indicating that a threshold temperature value has been exceeded (see column 3, lines 49-68; column 4, lines 1-55; column 5, lines 1-22);

(9) further comprising threshold adjustment logic in communication with the fail-safe sensor to increase the threshold temperature value to a new threshold temperature value in response to the fail-safe sensor indicating that the threshold

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temperature value has been exceeded (see column 3, lines 49-68; column 4, lines 1-55; column 5, lines 1-22);

(10) wherein the threshold adjustment logic is further to lower the new threshold temperature to detect decreases in temperature (see column 8, lines 36-49);

(11) further comprising an interrupt handler to display information regarding a temperature sensed by the fail-safe sensor to a user of the integrated circuit (see column 8, lines 36-49);

(16) further comprising controlling the temperature of the integrated circuit by increasing and decreasing a clock frequency of the integrated circuit in response to the sensed temperature (see column 3, lines 49-68; column 4, lines 1-55);

(17) further comprising executing instructions to provide closed loop control of the integrated circuit clock frequency in response to the sensed temperature (see column 8, lines 36-49);

(18) further comprising decreasing a clock frequency of the integrated circuit in response to the sensed temperature indicating that a threshold temperature value has been exceeded (see column 3, lines 49-68; column 4, lines 1-55; column 5, lines 1-22);

(19) further comprising displaying information regarding a sensed temperature to a user of the integrated circuit (see column 3, lines 49-68; column 4, lines 1-55).

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chuong A Luu whose telephone number is (571) 272-1902. The examiner can normally be reached on M-F (6:30-3:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-1975.

CAL  
March 5, 2004

  
**VUTHE SIEK**  
**PRIMARY EXAMINER**